

Fig. 1 (prior art)

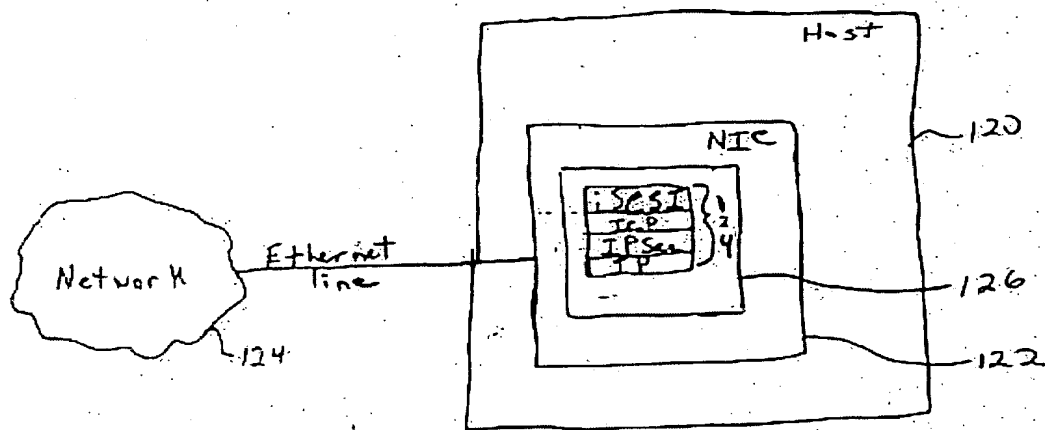


Fig. 2

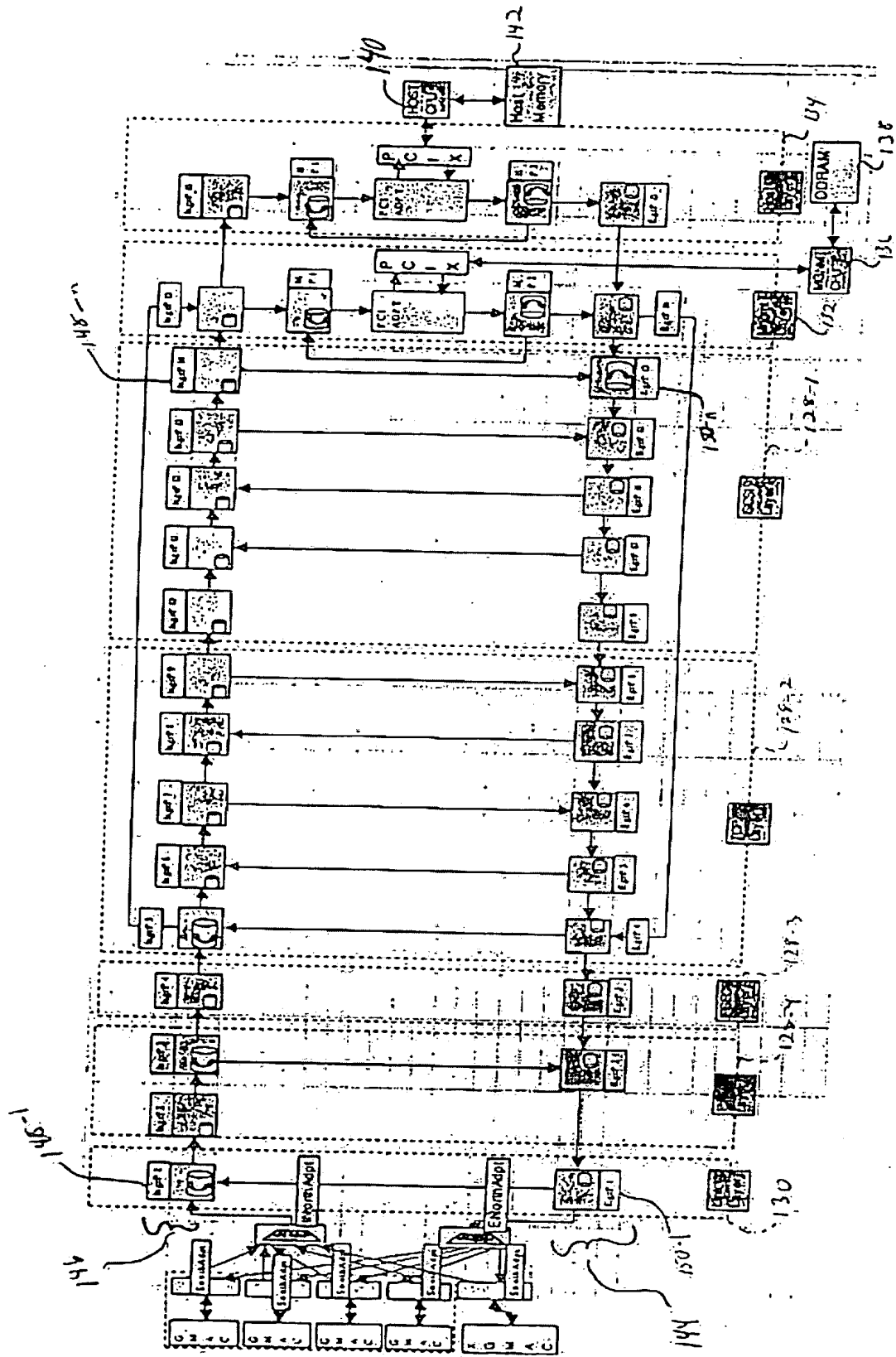
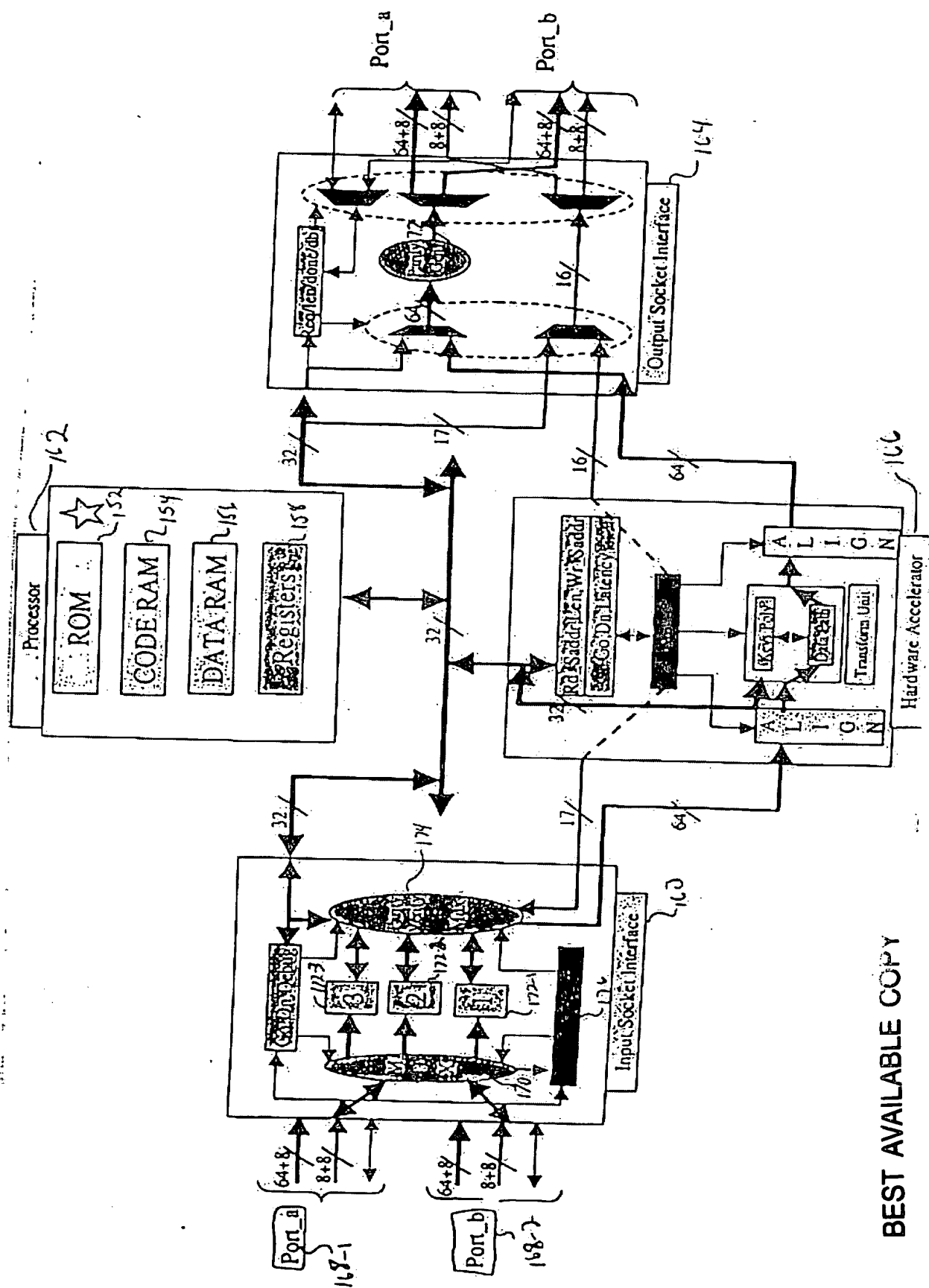


Fig. 3

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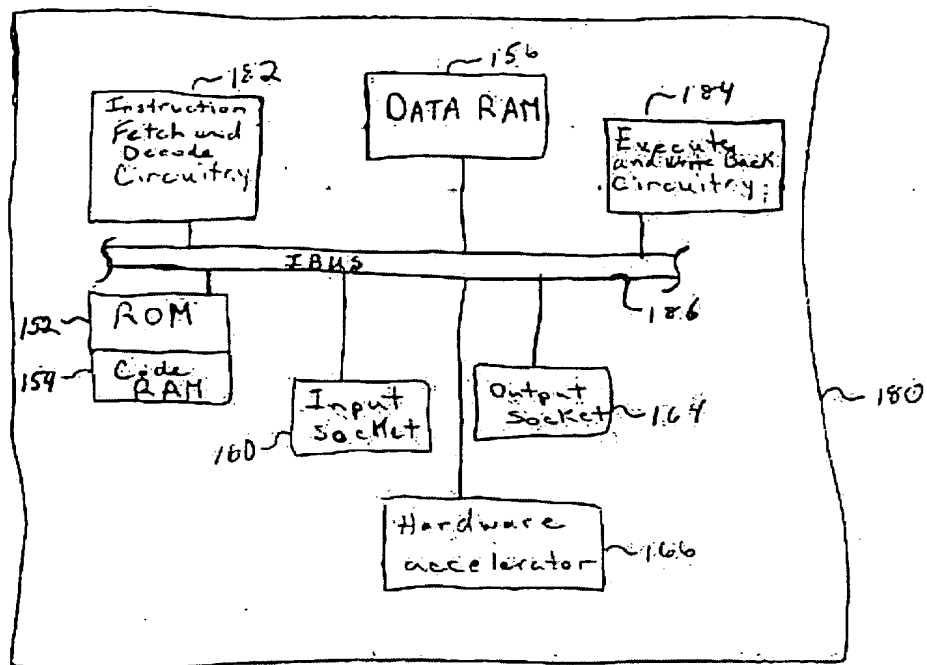
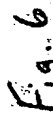


Fig 5



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I - instruction fetch and decode
II - execute and write back.

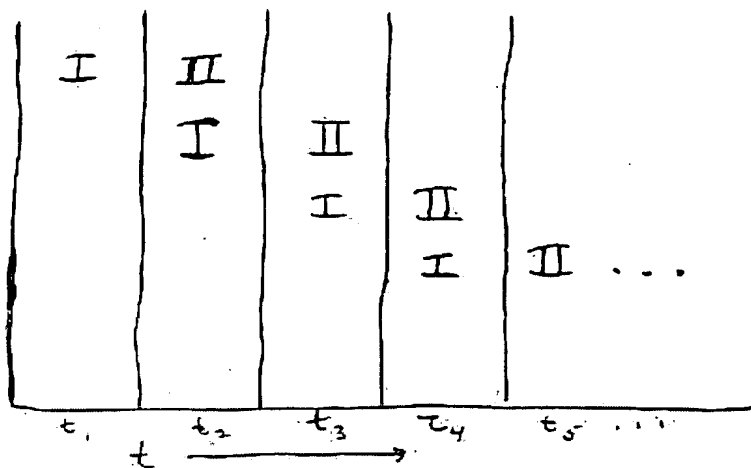


Fig. 7

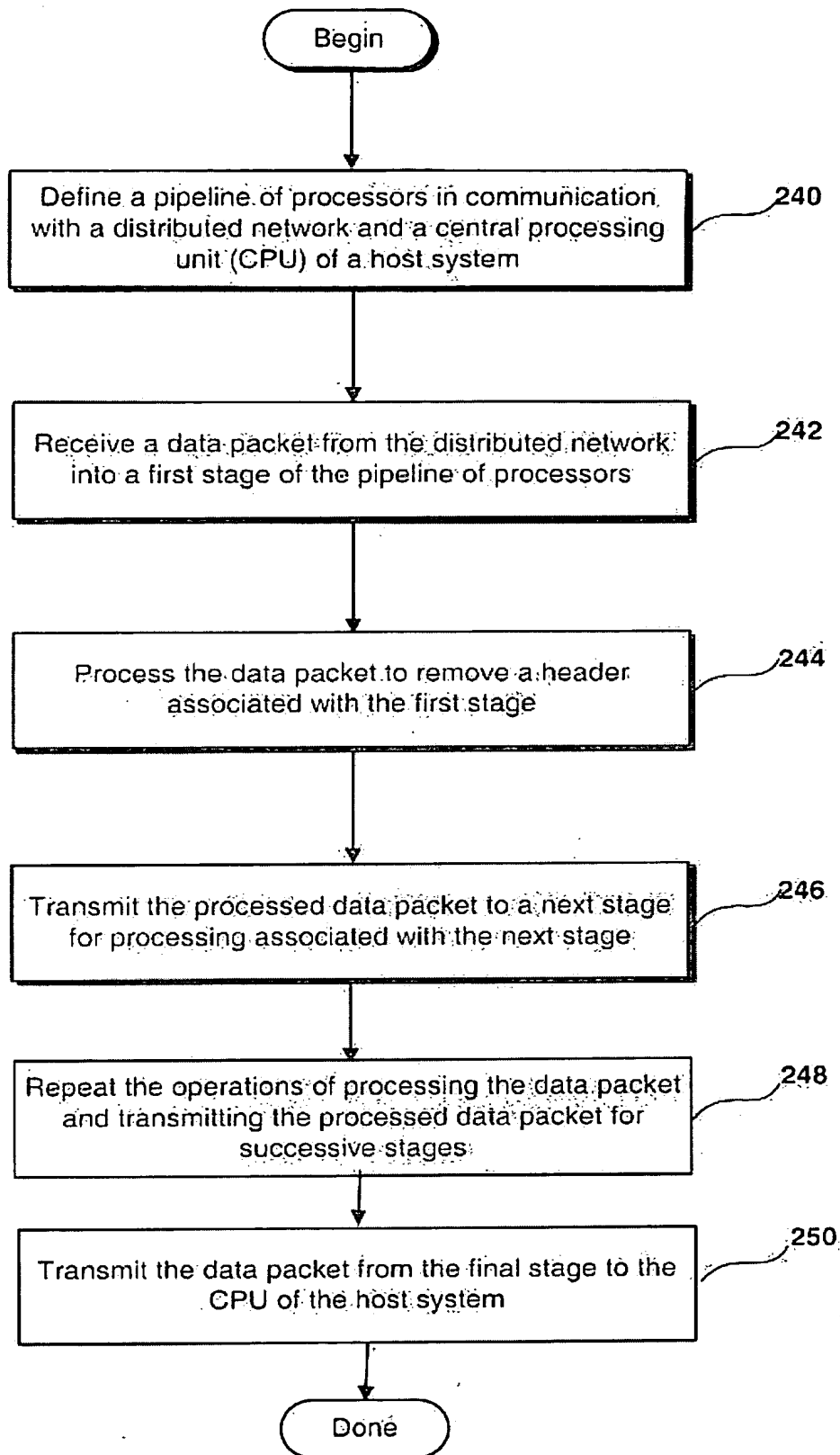


Fig. 8